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PATENT

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Applicant(s): Myeong-cheol Kim, et al.

Nadav, O. Examiner:

Serial No.:

09/731,385

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Title:

SEMICONDUCTOR DEVICE HAVING SELF-ALIGNED CONTACT AND

FABRICATING METHOD THEREFOR

CERTIFICATE OF MAILING UNDER 37 C.F.R. § 1.8

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Assistant Commissioner for Patents

Washington, D.C. 20231

AMENDMENT A

Sir:

This is in response to the Office Action mailed on March 13, 2002.

Please amend the application as follows:

In the Claims

Please cancel claims 12 and 13.

Please amend the claims as follows:

FECHNOLOGY CENTER 2801

(Amended) A semiconductor device having a self-aligned contact, the 1.

semiconductor device comprising:

a plurality of conductive patterns formed to be adjacent to one another by sequentially stacking and patterning a first conductive layer and a mask layer on a particular underlying layer;

a first insulation layer filling a gap between adjacent conductive layer patterns, the first insulation layer being formed of a first insulating material;